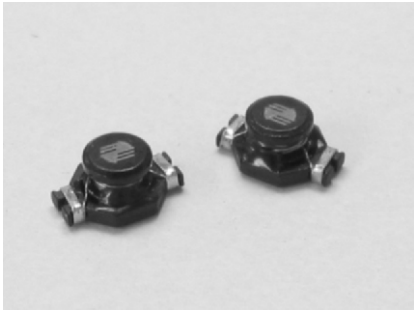
Elec & Eltek依利安達
E & E Magnetic Products Limited

SMT POWER INDUCTORS

Drum Type, UIS0403X Series, Self-Leaded

- Suitable for DC/DC conversions in notebook computers, PDAs and plamtops
- Low DCR and Low profile
- Unshielded and self-leaded design for pick and place handling
- Open construction provides stable inductance for increasing currents.
- RoHS compliant



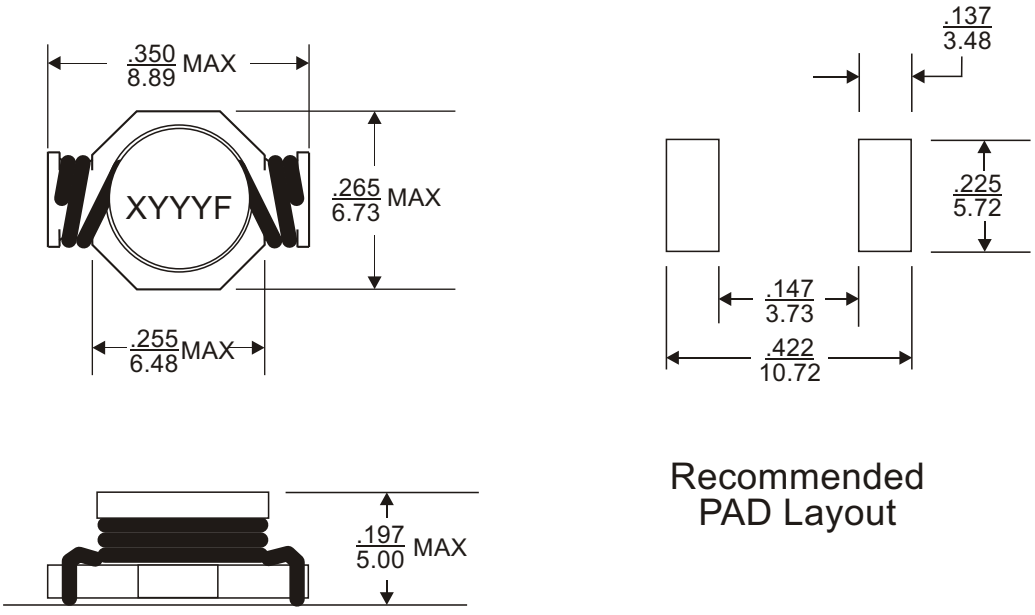
ELECTRICAL SPECIFICATION @ 25°C

Part Number	Inductance[2] L (H)	Inductance Tolerance(%) M	Test frequency (kHz)	DCR (m) Max	Isat[3] (A)	Irms[4] (A)	Marking (XYYY)
UIS0403M-561F	0.56	± 20	100	9.7	7.70	6.00	M561
UIS0403M-102F	1.00	± 20	100	14.0	5.30	4.40	M102
UIS0403M-152F	1.50	± 20	100	22.0	4.50	4.20	M152
UIS0403M-252F	2.50	± 20	100	24.5	3.50	3.50	M252
UIS0403M-332F	3.33	± 20	100	42.6	3.00	2.90	M332
UIS0403M-442F	4.44	± 20	100	51.5	2.50	2.20	M442
UIS0403M-233F	23.5	± 20	100	200	1.10	1.00	M233

Notes:

1. Ordering Information: UIS0403a - bbbFc.
   - UIS0403 = Product Type.
   - a = Tolerance of Inductance (M= ±20%).
   - bbb = Inductance value in uH (i.e. 561 = 0.56uH; 252 = 2.50uH; 233 = 23.5uH).
   - F = Internal Control Code.
   - c = Packaging Code (T = Tape & Reel Packaging in 13 inch).
2. Test frequency is specified as the frequency for measuring the inductance at 0.25Vrms.
3. Inductance drops 10% typical at Isat.
4. Temperature rise is 40°C typical at Irms.
5. Operating temperature range: -40°C to +125°C.
6. The part temperature (ambient temperature + temperature rise) should not exceed the upper limit of the operating temperature under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

MECHANICAL DIMENSIONS



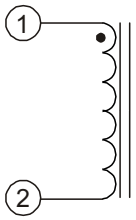
Recommended PAD Layout

Notes:

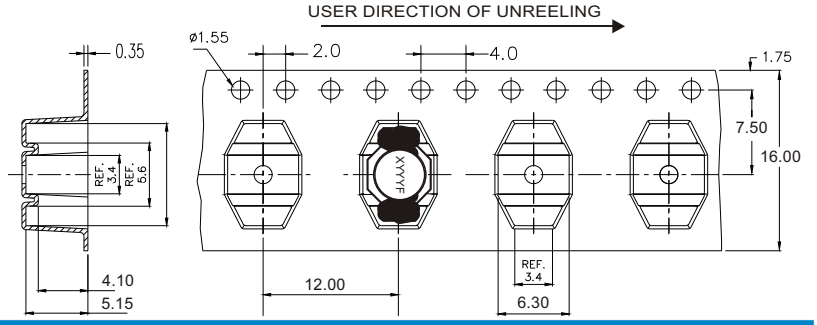
7. All dimensions are specified in inches / mm with higher precedence in inches.
8. Unless otherwise specified, all tolerances are ±.010 / 0.25.

Weight (in gram)	:	0.8 typ.
Tape & Reel	:	1000 / reel

SCHEMATIC



PACKAGING



FOR MORE INFORMATION, PLEASE CONTACT

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