ISO9001 & ISO14001 & TS16949 CHILISIN ELECTRONICS CORP.

SPECIFICATION FOR APPROVAL

Customer： _____

Customer P/N: _____

Drawing No： _____

Quantity： **0** **Pcs.** **Date：** _____

Chilisin P/N： **ACTG Series**

Automotive Grade Inductor

Halogen Free
RoHS Compliant
REACH Compliant
Lead Free Solders
AEC-Q200

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ACTG Series Specification

1 Scope:

1-1 This specification is applicable t to lead free and halogen free of ROHS directive for ACTG series anti-surge thick film chip resistors for automotive grade.

1-2 This product is for automotive electronic application.

1-3 AEC-Q200 qualified , grade 1

2 Part Numbering:

A C T G	0 0	0 6 0 3	-	100X	J	T P
Series	**Internal Code**	**Dimension (inch)**		**Resistance Value**	**Tolerance**	**Taping Code**
ACTG: Anti Surge Thick Film Chip Resistor for Automotive Grade		0402 0603 0805 1206 1210 2010 2512		J Tol: (3 digit + X) E-24 Series Ex. 10Ω = 100 47Ω = 470	J: ±5%	TH/10,000pcs: 0402 TP/5,000pcs: 0603, 0805, 1206,1210 TE/4,000pcs: 2010, 2512

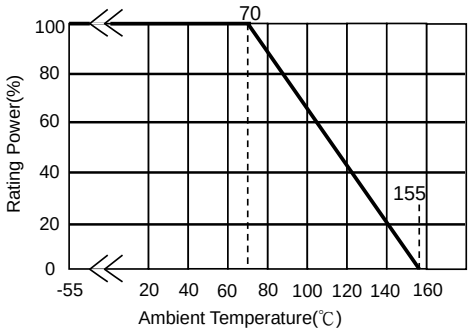
3 SPECIFICATIONS:

3.1 Resistance Range:

Type	Rated Power at 70℃	Max. Working Voltage	Max. Overload Voltage	T.C.R (ppm/℃)	Resistance Range J(±5%) E-24
0402	1/16W	50V	100V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
0603	1/4W	75V	150V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
0805	2/5W	150V	200V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
1206	1/2W	200V	400V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
1210	3/4W	200V	400V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
2010	3/4W	200V	400V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
2512	1W	200V	400V	±200	10Ω≦R≦1MΩ
				±400	1Ω≦R≦10Ω
Operating Temperature Range				−55℃ ~ +155℃	

3.2 Power Derating Curve:

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following.



ACTG Series Specification

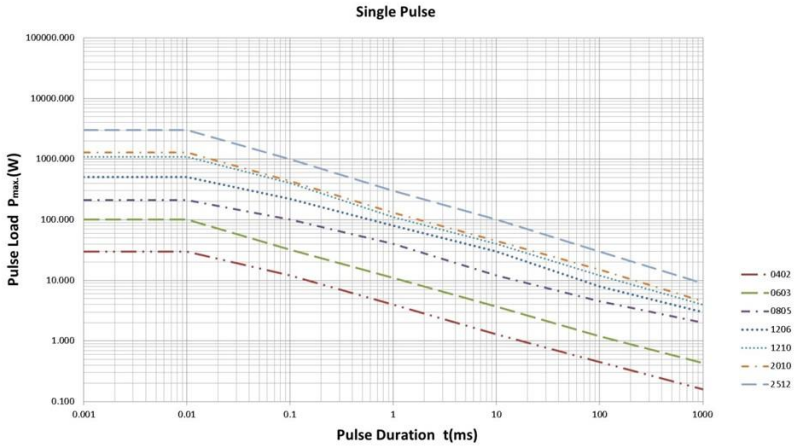
3.4 Voltage Rating

Rated Voltage: DC voltage or AC voltage (rms) based on the rated power
The voltage can be calculated by the following formula. If the calculated value exceeds the Max. voltage specified in the Table 3, the Max. voltage rating is set as the voltage rating.

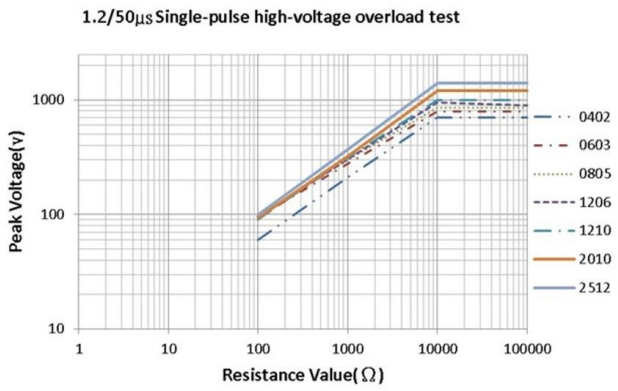
$$E = \sqrt{R \times P}$$

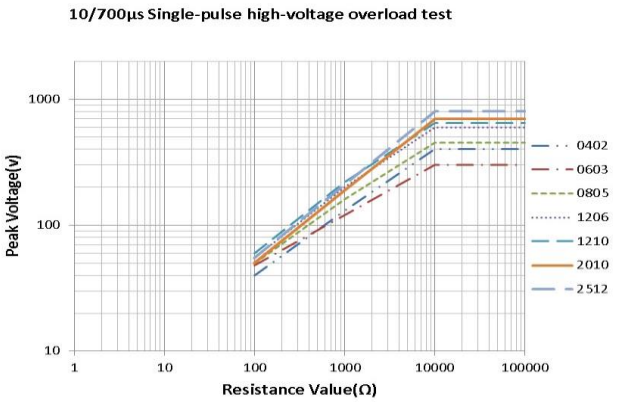
E= Rated voltage (v)
P= Power rating (w)
R= Nominal resistance(Ω)

3.5 Pulse Loading Capability
Pulse on a regular basis; maximum permissible peak pulse power (Pmax) as a function of a pulse duration.
V_{peak}≦0402(100V)、0603(150V)、0805(300V)、1206(400V)、2010(400V)、2512(400V)



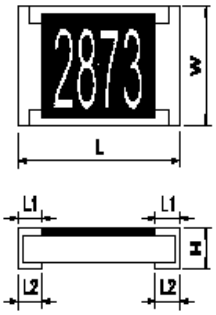
Lightning Surge Load:





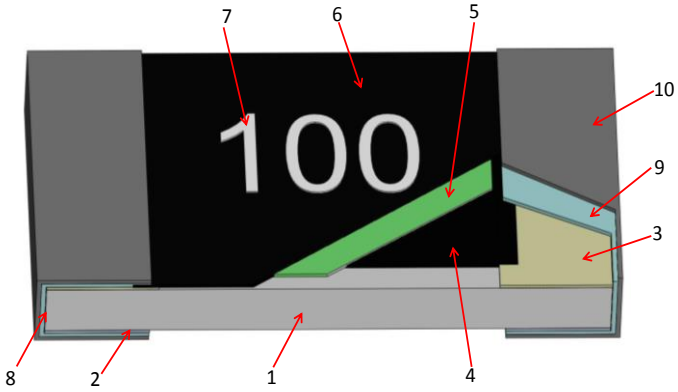
ACTG Series Specification

4 Dimensions:



Type \ Dimension	L	W	H	L1	L2
0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
1206	3.05±0.10	1.55±0.10	0.55±0.10	0.45±0.20	0.35±0.15
1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

ACTG Series Specification

6 Reliability Test:

No	Item	Conditions	Specifications Resistors
1-1	High Temperature Exposure (Storage)	Put the specimens in the chamber with temperature of 155±3℃ for 1000 hours. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	△R=±2.0% No mechanical damage.
1-2	Temperature Cycling	Put the specimens in the High & low temperature test chamber with temperature varies from -55℃ to 125℃ for 5 minutes and total 1000 cycles. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	△R=±2.0%
1-3	Short Time Overload	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications) Refer to JIS-C5201-1 4.13	△R=±2.0%
1-4	Biased Humidity	Solder the specimens on the test PCB and put them into the constant temperature humidity chamber with 85±2℃ and 85±5%RH. Then apply the test voltage that calculates based on the 10% of rated power for 1000hrs. Then take them out to stabilize in room temperature for 24±2hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	△R=±3.0%
1-5	Operational Life	Solder the specimens on the test PCB and Put them in the chamber with temperature of 125±3℃ and load the rated voltage for 1000 hours. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	△R=±3.0%
1-6	Resistance to Soldering Heat	The specimens are fully immersed into the Pb-free solder pot, then take them out to stabilize for 1 hour or more and measure of its resistance variance rate. Temp of solder pot：260±5℃ Soldering duration：10±1sec. Experiment evidence AEC-Q200	△R=±1.0%
1-7	ESD	Put the specimens on the test fixture and two (2)discharges (2KVDC) shall be applied to each PUT, one (1) with a positive polarity and one (1) with a negative polarity. Afterwards, the specimens stabilize for 30min or more and measure of its resistance variance rate. The test is performed with direct contact and regular discharge mode. The resistor and capacitor used on the spearhead is 2000Ω and 150pF respectively. Experiment evidence AEC-Q200	△R=± 3.0%
1-8	Solderability	Test method: Test item 1 (solder pot test): Method B Precondition: The specimens are subjected to 155℃ dry bake for 4hrs±15min. The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of 235± 5℃ for 5+0/-0.5 sec. Then rinse with water and observe the soldering coverage under the microscope. Test item 2 (Leaching test): Method D The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of 260±5℃ for 30+0/-0.5 sec. Then rinse with water and observe the soldering coverage under the microscope. Experiment evidence AEC-Q200	1.Soldering coverage over 95% 2.At the edge of terminal, the object underneath (e.g. white ceramic) shall not expose.

ACTG Series Specification

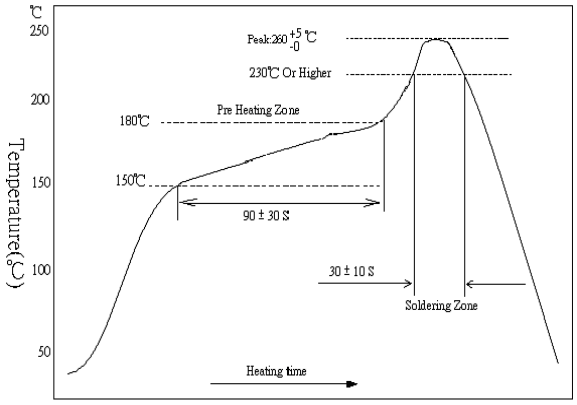
No	Item	Conditions of Test	Specifications	
			Resistors	Jumper
1-9	Electrical Characterization	TCR（ppm / ℃）= $\frac{(R2-R1)}{R1(T2-T1)}\times10^6$ R1: Resistance at room temperature (Ω) R2: Resistance at -55℃or +125℃(Ω) T1: Room temperature (℃) T2: Temperature -55℃or +125℃ Experiment evidence: AEC-Q200	Refer to item 3. general specifications	
1-10	Board Flex (Bending Test)	Solder the specimens on the test PCB and put the PCBA onto the Bending Tester. Add force at the central part of PCB, and the duration of the applied forces shall be 60 (+ 5) Sec. Measure of its resistance variance rate in load. Bending depth (D) 0402、0603、0805=5mm 1206、1210=3mm 2010、2512=2mm Experiment evidence: AEC-Q200	△R=±1.0% No mechanical damage, peel-off of side end or chip crack.	
1-11	Lightning surge test	Test 1: 5 pulses of 1.2/50 μs with a period of not less than 12s Test 2: 10 pulses of 10/700μs with a period of not less than 1 min. Refer to IEC 60 115-1 4.27	△R=±5.0%	

7 Recommend Soldering Method:

(This is for recommendation, please customer perform adjustment according to actual application)

7.1 Recommend Soldering Method:

7.1.1 Lead Free Reflow Soldering Profile



Remark: The peak temperature of soldering heat is 260 +5/-0 ℃ for 10 seconds

7.2 Soldering Iron: temperature 350℃±10℃, dwell time shall be less than 3 sec.

7.3Recommend Land Pattern Design (For Reflow Soldering)：

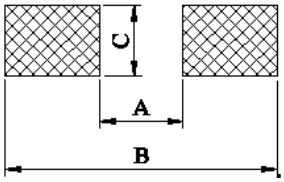
When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering.

When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

ACTG Series Specification

8 Recommend Land Pattern: (For Reflow Soldering)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



Unit : mm

TYPE \ DIM	A	B	C
0402	0.5	1.5	0.6
0603	0.8	2.1	0.9
0805	1.2	3	1.3
1206	2.2	4.2	1.6
1210	2.2	4.2	2.8
2010	3.5	6.1	2.8
2512	3.8	8	3.5

8.1 Automobile Electronic Application:

This specification is for automobile electronic use.
Chilisin will take no responsibility if any damage,.
cost or loss occurs when the product has been used
in any special circumstances
(a) Information、entertainment、navigation、audio control units.
(b) Comfortable door, window, seat control unit.
(c) Internal lighting control unit.

8.2 Environment Precautions:

This specification product is for general electronic use, CHILISIN will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with CHILISIN.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

(a) Used in high temperature and humidity environment
(b) Exposed to sea breeze or other corrosive gas, such as Cl2、H2S、NH3、SO2 and NO2.
(c) Used in non-verified liquids including water, oil, chemical and organic solvents.
(d) Using non-verified resin or other coating material to seal or coat our Company product.
(e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

8.3 Momentary Overload Precautions:

The product might be out of function when momentary overloaded.
Please make sure to avoid momentary overloading while using and preserving。

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8.4 Operation and Processing Precautions:

(a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.

(b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.

(c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.

(d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.

(e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

Storage and transportation requirement:

1.1 The temperature condition must be controlled as 25±5℃, the R.H. must be controlled as 60±15%. The stock can maintain quality level in two years.

1.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl2、H2S、NH3、SO2 and NO2.

1.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

9 Plating Thickness:

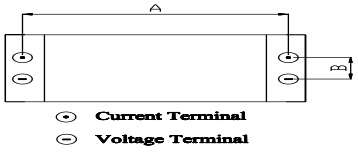
9.1 Ni:≧2μm

9.2 Sn(Tin):≧3μm

9.3 Sn(Tin):Matte Sn

10 Measurement Point:

Bottom electrode



Unit : mm

TYPE \ DIM	A	B
0402	0.80±0.05	0.24±0.05
0603	1.35±0.05	0.35±0.05
0805	1.80±0.05	0.35±0.05
1206	2.90±0.05	0.35±0.05
1210	2.90±0.05	0.35±0.05
2010	4.50±0.05	1.15±0.05
2512	5.90±0.05	1.60±0.05

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11 Stock period:

The temperature condition must be controlled at 25±5℃, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.

12 The carton for electronic-information products is made by the symbol as follows: (For china)

	
Marking for control of pollution cause by electronic-information products	Marking for package recovery

ACTV Series Specification

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